

LSI LOGIC

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Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date March 11, 2005Number of pages including cover sheet 2**To: US PTO****Mail Stop Status Request**

TC 2133

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**From: Connie del Castillo****Intellectual Property Paralegal**Telephone No. **(408) 433-7191**Fax No. **(408) 433-7460****REMARKS:**☐ Urgent☐ For your review☒ Reply ASAP☐ Please commentApplication Number: 09/975,293  
Filing date: October 11, 2001  
First named inventor: Max Yeung et al.  
Attorney docket number: 01-322 / 1496.00144

Transmitted herewith for filing via facsimile:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Max Yeung et. al.

**Serial No. :**

09/975,293

**Filed :**

October 11, 2001

**For :**

# Construction Of An Optimized SECDED Code and Logic For Soft Errors In Semiconductor Memories

**Group Art Unit :**

2133

**Examiner :**

Torres, Joseph D.

**Atty Docket :**

1496.00144 / 01-322

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## STATUS REQUEST

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

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3/11/05

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